

**INFORMATION HANDLING SYSTEM  
CAPABLE OF OPERATION IN REDUCED POWER STATES**

Abstract of Disclosure

**[0029]** A method for allowing a processor to enter low power states in an information handling system (IHS) includes detecting an access request for a bus mastering device. The method also includes in response to failing to detect an access request for the bus mastering device within a period of time, suspending a bus mastering device controller associated with the bus mastering device, wherein the now suspended bus mastering controller no longer prevents the processor from entering low power states.